

IN THE TITLE:

Please delete the title and insert therefor:

--RECTIFIER WITH ALTERNATIVE PATH FOR FREEWHEELING CURRENT--.

IN THE CLAIMS:

Claim 1 (amended). A rectification circuit comprising:

a diode full-wave bridge rectifier;

a freewheeling current path comprising at least one, but not more than two, freewheeling diodes coupled substantially across said bridge rectifier;

blocking means coupled between said rectifier and said freewheeling current path for causing freewheeling current of said bridge rectifier to substantially flow through said freewheeling current path.

Please cancel Claims 3-4.

Claim ~~3~~³ (amended). A rectification circuit as recited in Claim [3] 1, further comprising at least one capacitor, each said capacitor coupled across a respective one of said at least one, but not more than two, freewheeling diodes.

Claim ~~4~~⁴ (amended). A rectification circuit as recited in Claim [3] 1, further comprising at least one filtering circuit, each said filtering circuit coupled to a respective one of said at least one, but not more than two, freewheeling diodes to limit the rate of rise of reverse voltage across said at least one, but not more than two, freewheeling diodes.

Claim 5⁶ (amended). A rectification circuit comprising:
a diode full-wave bridge rectifier;

a freewheeling current path comprising at least one, but not more than two, freewheeling diodes coupled substantially across said bridge rectifier;

a blocking diode coupled between said rectifier and said freewheeling current path to cause freewheeling current of said bridge rectifier to at least partially flow through said freewheeling current path.

Please cancel Claims 8 and 9.

10⁶. A rectification circuit as recited in Claim 8, further comprising at least one capacitor, each said capacitor coupled across a respective one of said at least one, but not more than two, freewheeling diodes.

11⁷. A rectification circuit as recited in Claim 8, further comprising at least one filtering circuit, each said filtering circuit coupled to a respective one of said at least one, but not more than two, freewheeling diodes to limit the rates of rise of reverse voltage across said at least one, but not more than two, freewheeling diodes.

Please cancel Claim 12.

8, Claim ~~13~~ (amended). A rectification circuit comprising:

an electrical load;

a plurality of first diodes coupled across a first node and a second node in a bridge rectifier configuration;

at least one second diode coupled between said first node and a third node; and

a third diode coupled between said second node and said third node;

[A rectification circuit as recited in Claim 12,]
wherein said third diode is connected in order to:

allow current rectified by said first diodes to flow to said load; and

prevent at least some freewheeling current flowing through said load from flowing through said first diodes and instead cause said at least some freewheeling current to flow through said at least one second diode[s].

9.
Claim ~~14~~ (amended). A rectification circuit as recited in Claim ~~13~~, further comprising at least one filtering circuit, each said filtering circuit coupled to a respective one of said at least one second diode[s] to limit the rates of rise of reverse voltage across said at least one second diode[s].

R E M A R K S

Claims 1-14 are pending in the present application. Claims 1-12 and 14 stand rejected, and Claim 13 stands objected to as dependent upon a rejected base claim. After entry of the